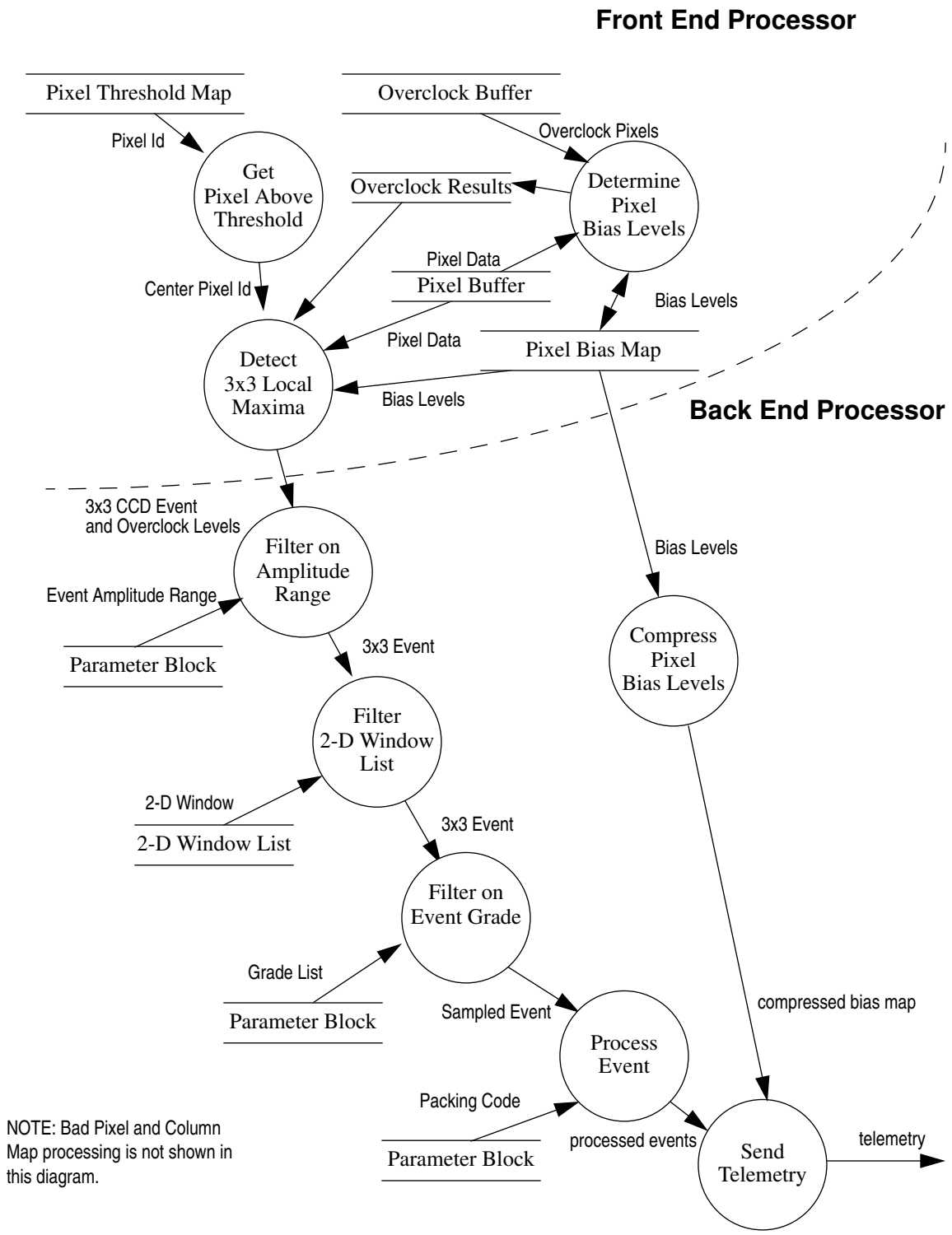
Front End Processor
Pixel Threshold Map
Overclock Buffer
Pixel Id
Overclock Pixels
Get Pixel Above Threshold
Overclock Results
Determine Pixel Bias Levels
Pixel Data
Pixel Buffer
Center Pixel Id
Bias Levels
Pixel Data
Pixel Bias Map
Detect 3x3 Local Maxima
Bias Levels
Back End Processor
3x3 CCD Event and Overclock Levels
Filter on Amplitude Range
Bias Levels
Event Amplitude Range
Parameter Block
3x3 Event
Compress Pixel Bias Levels
Filter 2-D Window List
2-D Window
2-D Window List
3x3 Event
Filter on Event Grade
Grade List
Parameter Block
Sampled Event
compressed bias map
Process Event
Packing Code
Parameter Block
processed events
Send Telemetry
telemetry
NOTE: Bad Pixel and Column Map processing is not shown in this diagram.