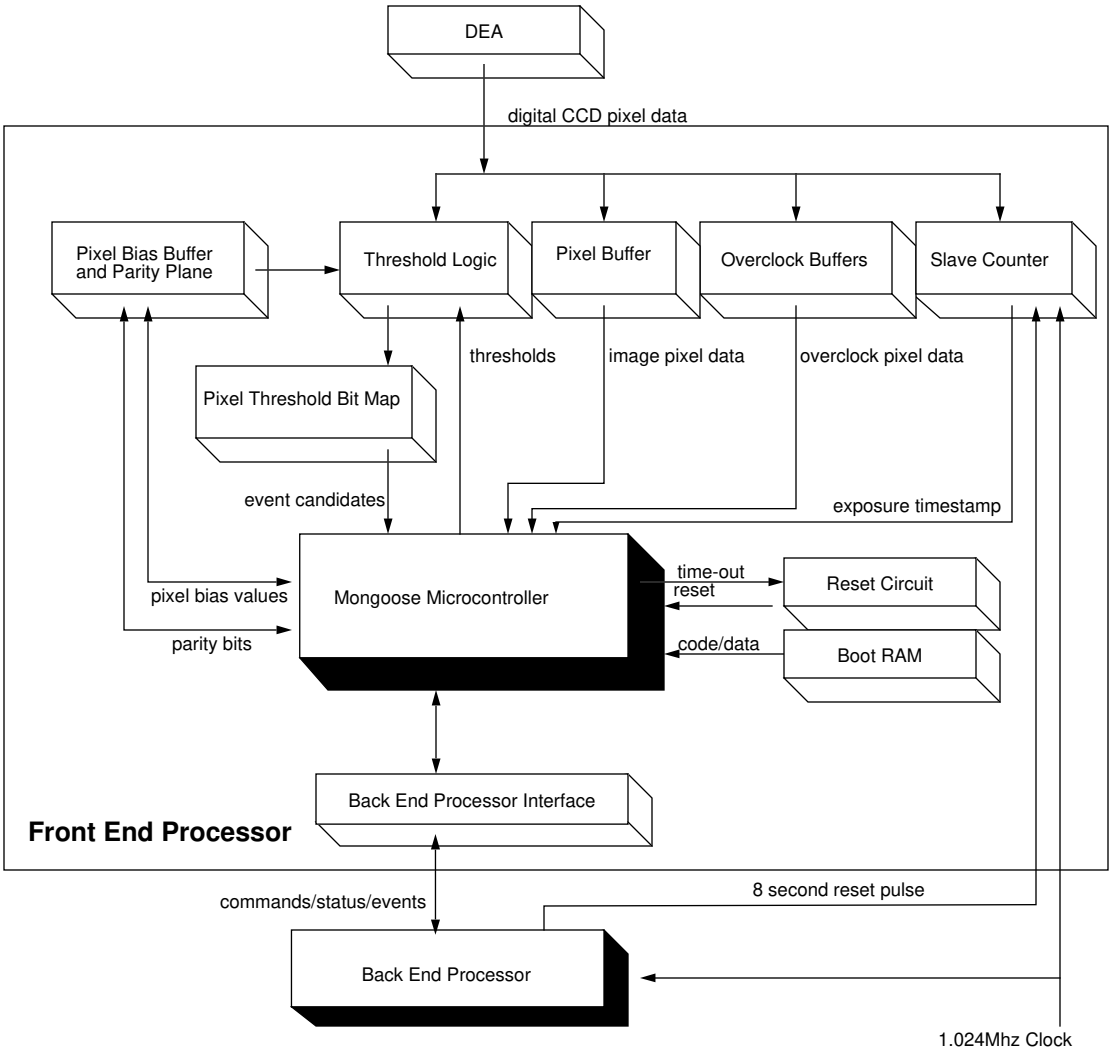
DEA
digital CCD pixel data
Pixel Bias Buffer and Parity Plane
Threshold Logic
Pixel Buffer
Overclock Buffers
Slave Counter
thresholds
image pixel data
overclock pixel data
Pixel Threshold Bit Map
event candidates
exposure timestamp
pixel bias values
parity bits
Mongoose Microcontroller
time-out
reset
Reset Circuit
code/data
Boot RAM
Back End Processor Interface
Front End Processor
commands/status/events
8 second reset pulse
Back End Processor
1.024Mhz Clock