TABLE 1. Write Front End Memory Command Packet

Req	Item	Description
1-1	Packet Length	Length of command packet in 16-bit words
1-2	Packet Identifier	Used to identify the command
1-3	Command Opcode	Write_FEP_Memory_Command opcode
1-4	Front End Processor Id	This identifies which Front End Processor to write to.
1-5	Virtual Address to Write To	32-bit Back End Processor Virtual Address to start writing to. This address must be long word aligned (i.e. evenly divisible by 4). If not, the instrument will reject the command.
1-6	Data to Write	The array of 32-bit data words to write. The number of 32-bit words to write is determined by the Packet Length.