TABLE 1. System Configuration Table Layout

Index	Name	Quantity	Description
0	DEA Power	1	This is a bit-field, indicating which CCD-controllers should be powered. Each bit is indexed by CCD Id (i.e. bit 0 corresponds to CCD_I0). If a bit is '1', then the CCD controller should be on, otherwise, it should be off. Unused bits in the field should be set to zero.
1	FEP Power	1	This is a bit-field, indicating which FEPs should be powered. Each bit is indexed by FEP Id (i.e. bit 0 corresponds to FEP_0). If a bit is '1', then the FEP should be on, otherwise, it should be off. Unused bits in the field should be set to zero.
2..65	DEA Interface Controller Settings	64	This is an array of interface board settings. The assignments are TBD.
66+ (64 * *ccdId*) + *settingId*	DEA CCD Controller Settings	10 * 64	This is a set of arrays of CCD Controller settings. The index formula indicates the offset to a particular setting, indicated by *settingId* for a particular CCD Controller, indicated by *ccdId*. The register assignments are TBD.