TABLE 1. Back End Processor Interrupts

Priority (0 highest)	Input	Name	Causes	Response Time	Frequency
0	INT0	Command Interrupt	Command Ready, Length Error, FIFO Full	< 1ms	peak 4/second avg. 1/hour
1	INT1	DTC Interrupt	Telemetry transfer complete	Goal < 0.5ms	peak 100/second avg. 3/second
2	INT3[a]	Timer Tick	Timer Count expired	< 1ms	10/second
3	INT3[b]	DMA Done	DMA Transfer Complete	< 1ms	Varies on transfer length
4	INT2	FEP Interrupt	One or more FEPs requesting service	< 10ms	peak 60/second avg. 10/second
5	INT4	RadMon	Radiation Monitor Asserted	< 10ms	peak 4/second

a. Mongoose Extended Interrupt - Timer EOC (see Section 4.2.5)

b. Mongoose Extended Interrupt - DMA Done (see Section 4.2.5)