TABLE 1. DEA Memory Map

Device/ Memory Section	Address Range	Description
SRAM	0:0x7fff	Sequencer RAM (see Section 4.8.3.5 for the word format)
PRAM	0x8000:0xffff	Program RAM (see Section 4.8.3.4
Subsystem Registers	0x10000:0x1003f	Collection of various control/status registers Assignments TBD
DAC Registers	0x10040:0x1005f	Collection of Digital-to-Analog Converters (Write-only) Assignments TBD
Housekeeping Registers	0x10008:0x100ff	Collection of Housekeeping devices (Read-only) Assignments TBD