TABLE 1. I-cache Bad Pixel and Column Map Layout (TBD)

Region		Address	Byte Size	Description
Patch Area		0x800fffff 0x800cfc00	0x30400	
Bad Pixel Map	Entries	0x800c5c04	0x9ffc	Array of bad pixel map entries (max. 10239)
	Count	0x800c5c00	0x4	Number of 32-bit bad pixel map entries in the table
Continuous Clocking Bad Column Map	Entries	0x800c4404	0x17fc	Array of bad column map entries (max. 3070 entries)
	Count	0x800c4400	0x4	Number of 16-bit bad column map entries in the table
Timed Exposure Bad Column Map	Entries	0x800c2c04	0x17fc	Array of bad column map entries (max. 3070 entries)
	Count	0x800c2c00	0x4	Number of 16 bit bad column map entries in the table
Parameter Blocks		0x800c0400	0x2800	
System Configuration		0x800c0000	0x400	
Code		0x80080000	0x40000	